

<b>Notice of References Cited</b>	Application/Control No. 09/544,492	Applicant(s)/Patent Under Reexamination PORTER, SWAIN W.	
	Examiner Lewis A. Bullock, Jr.	Art Unit 2195	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Gareau, Jean. "Advanced Embedded X86 Programming: Protection and Segmentation." May 1998.
	V	Isa, Haruna R. et al. "A Multi-threading Architecture for Multilevel Secure Transaction Processing." IEEE. 1999.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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